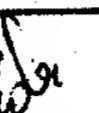


		Subclass				PATENT NUMBER
		Class				
		ISSUE CLASSIFICATION				
						
<b>U.S. UTILITY Patent Application</b>						
 <b>O.I.P.E.</b> SCANNED <u>09/93</u> Q.A. <u>GW</u>			<b>PATENT DATE</b>			
APPLICATION NO. 09/785452	CONT/PRIOR F	CLASS 257	SUBCLASS 673	ART UNIT <del>2811</del> <del>2815</del>	EXAMINER <del>EDDIE LEE</del>	
APPLICANTS Yoshinori Miyaki Hiromichi Suzuki Tsuyoshi Kaneda						
TITLE Semiconductor integrated circuit device and method of manufacturing the same						
PTO-2040 12/99						

# **ISSUING CLASSIFICATION**

<b>ORIGINAL</b>		<b>CROSS REFERENCE(S)</b>					
<b>CLASS</b>	<b>SUBCLASS</b>	<b>CLASS</b>	<b>SUBCLASS (ONE SUBCLASS PER BLOCK)</b>				
<b>INTERNATIONAL CLASSIFICATION</b>							

<b>TERMINAL DISCLAIMER</b>		<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
		<b>Sheets Drwg.</b>	<b>Figs. Drwg.</b>	<b>Print Fig.</b>	<b>Total Claims</b>	<b>Print Claim for O.G.</b>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.					<b>NOTICE OF ALLOWANCE MAILED</b> <hr/> <hr/> <hr/>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____					<b>ISSUE FEE</b> <hr/> <hr/> <hr/>	
					<b>Amount Due</b>	<b>Date Paid</b>
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.					<b>ISSUE BATCH NUMBER</b> <hr/> <hr/> <hr/>	
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